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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/923670	CONT/PRIOR F	CLASS 38 257	SUBCLASS 499	ART UNIT 2835	EXAMINER Thomas
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2822

Integrated circuit devices having dielectric regions protected with multi-layer insulation structures and methods of fabricating same

PTO-2040
12/89

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